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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/690,263	10/21/2003	Dominik J. Schmidt		4606

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EXAMINER

FRANKLIN, RICHARD B

ART UNIT	PAPER NUMBER
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2182

DATE MAILED: 09/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/690,263

Applicant(s)

SCHMIDT, DOMINIK J.

Examiner

Richard Franklin

Art Unit

2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 20 May 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

RJ

DETAILED ACTION

1. Claims 1 – 10 have been examined.

Drawings

2. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: 190 (Fig 5), 134 (Fig 5), 132 (Fig 5), 140 (Fig 5). Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character(s) in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

3. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: "buffer 300" (Page 9 Line 22, Page 10 Line 4). Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should

Art Unit: 2182

include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claim 8 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 8 recites the limitation "the internal bus" in line 23. There is insufficient antecedent basis for this limitation in the claim.

Examiner has assumed that applicant intends Claim 8 to depend on Claim 6, at which point, the internal bus of Claim 8 is taken to refer to the internal bus of Claim 6.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

Art Unit: 2182

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Moss et al. US Patent No. 5,793,989.

Moss et al. teach a system that allows for the dual function of a peripheral card. The system comprises a first and second bus interface that conforms to a first and second bus standard (Fig 1, Col 2 lines 32 – 38, Col 2 line 64 – Col 3 line 19). A pin set is coupled to the first and second bus interface and are selectable to communicate with a host computer bus in accordance with either the first or second bus standard (Col 3 line 20 – 30).

6. Claims 2, and 4 – 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Moss et al. US Patent No. 5,793,989.

Moss et al. teach a system that allows for the dual function of a peripheral card as described per Claim 1. As per Claim 2, the set of pins that is coupled to the first and second bus standard is that of the Personal Computer Memory Card International Association (PCMCIA) bus standard (Fig 1, Col 2 Lines 32 – 38). As per Claim 4, one of the bus standards described is the PCMCIA bus standard (Fig 1, Col 2 Lines 32 – 38). As per Claim 5, the pins of the connector are connected to a multi-voltage buffer (Col 45 Lines 45 – 53). As per Claim 6, the first and second circuits are connected to internal busses (Fig 1, Col 2 Lines 32 – 38).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Moss et al. US Patent No. 5,793,989 in view of Tyson "How PCI Works."

Moss et al. teach a system that allows for the dual function of a peripheral card as described per Claim 1.

Moss et al. do not teach that one of the bus standards is the Peripheral Component Interconnect (PCI) bus standard.

Tyson teaches that computers use the PCI bus standard to connect to peripherals (Tyson; Page 4 Paragraph 1).

It would have been obvious to one of ordinary skill in the art at the time of invention to include the PCI bus standard in the system of Moss et al. Using the PCI bus standard in a peripheral allows for utilization of Plug-and-Play (PnP) with the device (Tyson; Pages 5 – 6).

8. Claims 7 and 8 rejected under 35 U.S.C. 103(a) as being unpatentable over Moss et al. US Patent No. 5,793,989.

Moss et al. teach a system that allows for the dual function of a peripheral card as described per Claim 6.

Moss et al. does not expressly teach that the first or second interface circuits format signals on the internal busses to be compliant with the first or second bus standard.

It would have been obvious to one of ordinary skill in the art at the time of invention to see that the first and second interface circuit of Moss et al. interprets signals on the internal busses to be compliant with the first and second bus standard.

9. Claims 9 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Moss et al. US Patent No. 5,793,989 in view of Cahill et al. US Patent No. 6,871,244 B1.

Moss et al. teach a system that allows for the dual function of a peripheral card as described per Claim 1.

Moss et al. do not teach where the system of Claim 1 comprises a first or second power supply to supply voltage swings in accordance with the first or second bus standard.

Cahill et al. teach a system of selecting a bus standard to use similar to that of Moss et al. Cahill et al. also teach where in the selection, a power conversion is performed when needed by a power supply to convert signal voltages to those appropriate for the bus standard (Cahill et al.; Fig 3, Col 6 Lines 20 – 40).

It would have been obvious to one of ordinary skill in the art at the time of invention to include the power supply of Cahill et al. in the system of Moss et al. to supply voltages that are compliant with the appropriate bus standard (Cahill et al.; Col 6 Lines 29 – 33).

Art Unit: 2182

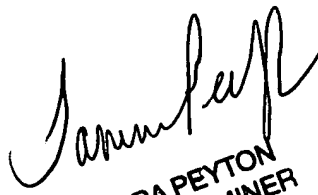
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard Franklin whose telephone number is (571)272-0669. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dov Popovici can be reached on (571)272-4083. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Richard Franklin
Patent Examiner
Art Unit 2182



TAMMARA PEYTON
PRIMARY EXAMINER